UA648480A-WRNNAN$

優潔光科技

UJ Light Technologies

Feature

1. 648×480 pixels display
2. High cntrast High reflectance
3. Ultra wide viewing angle Ultra low power consumption
   Pure reflective mode Bi-stable display
4. Commercial temperature
5. Hard-coat antiglare display surface
6. Ultra Low current deep sleep mode On chip display RAM

Mechanical Data

Item	Standard Value	Unit
Screen Size	5.83	Inch
Display Format	648.0 x 480.0	Pixel
Active Area	118.78 x 88.22	mm
Outline Dimension	125.4 x 99.5 x 1.2	mm

Electronic Characteristics

Parameter	Symbol	Rating	Unit
Logic supply voltage	VCI	-0.3 to +6.0	V
Logic Input voltage	VIN	-0.3 to VCI +0.3	V
Operating Temp range	TOPR	0 to +40	ºC.
Storage Temp range	TSTG	-25 to+40	ºC.
Optimal Storage Temp	TSTGo	23±3	ºC.
Optimal Storage Humidity	HSTGo	55±10	%RH

Pin Assignment

No.	Name	Description	No.	Name	Description
1	NC	Do not connect with other NC pins	13	SCL	Serial Clock pin (SPI)
2	GDR	N-Channel MOSFET Gate Drive Control	14	SDA	Serial Data pin (SPI)
3	RESE	Current Sense Input for the Control Loop	15	VDDIO	Power Supply for interface logic pins It should be connected with
4	NC	Do not connect with other NC pins	16	VCI	Power Supply for the chip
5	VSH2	Positive Source driving voltage(Red)	17	VSS	Ground
6	TSCL	I2C Interface to digital temperature sensor Clock pin	18	VDD	Core logic power pin VDD can beregulated internally from VCI.
7	TSDA	I2C Interface to digital temperature sensor Data pin	19	VPP	FOR TEST
8	BS1	Bus Interface selection pin	20	VSH1	Positive Source driving voltage
9	BUSY	Busy state output pin	21	VGH	Power Supply pin for Positive Gate driving voltage and VSH1
10	RES#	Reset signal input. Active Low.	22	VSL	Negative Source driving voltage
11	D/C#	Data /Command control pin	23	VGL	Power Supply pin for Negative Gate driving voltage VCOM and
12	CS#	Chip select input pin	24	VCOM	COM driving voltage

Dimension